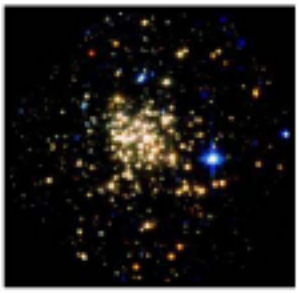
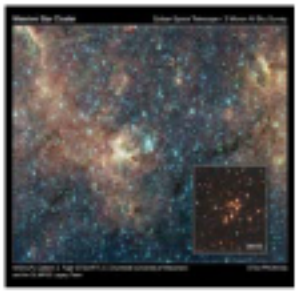
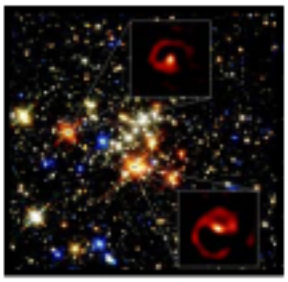
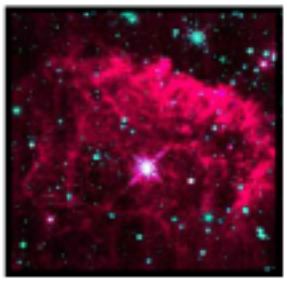
Rochester
Imaging Detector Laboratory
One Lomb Memorial Drive, Rochester, NY 14623-5603

Hardware Programmer

The Rochester Imaging Detector Laboratory (RIDL), within the Center for Imaging Science at the Rochester Institute of Technology seeks to hire software programmers with skills in C/C++, DSP assembler, ASIC, and FPGA programming. The RIDL develops, and uses, cutting-edge detectors that utilize advanced CMOS readout circuitry. The RIDL is a development lab for the Large Synoptic Survey Telescope (LSST) and Supernovae Acceleration Probe (SNAP), two prominent astrophysics space missions for the coming decade. The RIDL is also pursuing its own orbital and suborbital rocket science program.

The Hardware Programmer will support detector data acquisition at the hardware level. This includes programming in DSP assembler, custom assemblers, ASIC/FPGA microcode, and C/C++. The appointment can be in the form of an internship, masters/PhD thesis topic, or a term appointment. Applicants must have a Bachelor's degree in computer science, electrical engineering, or related fields (Master's is a plus) and have experience with software programming. Additional mathematical, statistical and computer skills are desirable.

Inquiries about this position may be directed to Donald F. Figer (figer@cis.rit.edu).